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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/625,843	07/26/2000	Hiroki Hiyama	35.C14640	7974

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EXAMINER

AGGARWAL, YOGESH K

ART UNIT PAPER NUMBER

2622

DATE MAILED: 05/26/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/625,843

Applicant(s)

HIYAMA ET AL.

Examiner

Yogesh K. Aggarwal

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 06 March 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 14, 16 and 17 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 14, 16 and 17 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 03/06/2006 has been entered.

Response to Arguments

2. Applicant's arguments with respect to claims 14, 16 and 17 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 14, 16 and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hiyama et al. (US Patent # 6,963,372) in view of (USPN 4,870,495 to Kinoshita et al.).

[Claim 14]

Hiyama teaches a driving method for an image pickup device having pixels each including a photoelectric conversion unit (figure 14, pixel PD), a transfer switch (TX) for transferring a photoelectric conversion signal charges generated by said photoelectric conversion unit to a floating diffusion charge accumulator at an input terminal of an amplifier element (amplification transistor M3 inherently has a forward diffusion present at an input terminal), wherein the image

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pickup device includes signal lines (V1 and V2 as shown in figure 13) outputting the amplified signal to a capacitor (CTN and CTS, figure 13) arranged at each signal line and a switch element (M5) for controlling electric continuity of the signal line and the capacitor (col. 15 lines 4-17, col. 5 lines 45-59), comprising

a driving step of applying a pulse transfer switch (e.g. a transfer pulse ϕ_{TX1} shown in figure 15) to transfer the signal charge generated by said photoelectric conversion unit to the floating diffusion charge accumulator before reading out a signal from the pixel to the signal line (select pulse ϕ_{SEL1} applied after ϕ_{TX1}) and applying one pulse to the switch element after applying a pulse to the transfer switch (e.g. ϕ_{TS} , col. 16 lines 11-67).

Hiyama does not disclose nor preclude applying a plurality of transfer pulses to the transfer switch. Kinoshita discloses to apply a plurality of transfer pulses to a transfer switch so that the charge transfer can be reliably carried out (e.g., column 6, lines 32-36). Therefore it would have been obvious to one of ordinary skill in the art at the time of the invention to apply a plurality of transfer pulses to Hiyama's transfer switch in order for the charge transfer to be reliably carried out.

[Claims 16 and 17]

Hiyama teaches a ϕ_{RES} pulse (figure 15) being applied to a reset transistor (M1), then the gate of the pixel amplifier M3 is reset. A signal ϕ_{TX1} becomes high at time t_{75} , and photo-charge is transferred to the gate of pixel amplifier (col. 17 lines 1-17) and thereafter the ϕ_{SEL1} and ϕ_{TS} are changed to high at time t_{78} and photocharges are read out. The differential block 73 takes the difference between V_{1S} to V_{nN} and the corresponding noise signals V_{1N} to

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VnN, and sequentially outputs the differences as a voltage VOUT (col. 17 lines 35-47). This process is commonly known as CDS and the signals are called correlated signals.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Yogesh K. Aggarwal whose telephone number is (571) 272-7360. The examiner can normally be reached on M-F 9:00AM-5:30PM.

5. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Ometz can be reached on (571)-272-7593. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

6. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

YKA
May 20, 2006


DAVID OMETZ
SUPERVISORY PATENT EXAMINER